

HIGH SPEED DATA BUS

Abstract of the Disclosure

The invention comprises data processing systems and components thereof. Such systems may include a memory controller, a plurality of memory devices, a data bus
5 coupling the memory controller with the plurality of memory devices, and at least one bus switch located in the data bus between the memory controller and one of the plurality of memory devices. Memory integrated circuits and memory modules including at least one switch in the data bus are also provided.

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